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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/803,850

03/17/2004

Li Liu

TRQ-12924-1D

6425

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7590

10/01/2004

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EXAMINER

MOTTOLA, STEVEN J

ART UNIT

PAPER NUMBER

2817

DATE MAILED: 10/01/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

**Office Action Summary**

Application No.

10/803,850

Applicant(s)

LIU ET AL.

Examiner

Steven J. Mottola

Art Unit

2817

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

- 1) ☒ Responsive to communication(s) filed on 17 August 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

- 4) ☒ Claim(s) 39,40,42-59 and 61-65 is/are pending in the application.
- 4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 64 and 65 is/are allowed.
- 6) ☒ Claim(s) 39,40,42-46,50-55 and 59 is/are rejected.
- 7) ☒ Claim(s) 47-49,56-58,61 and 62 is/are objected to.
- 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 17 August 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some \* c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- \* See the attached detailed Office action for a list of the certified copies not received.

**Attachment(s)**

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date 081704.
- 4) ☐ Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: \_\_\_\_\_

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

Claim 63 is rejected under 35 U.S.C. 102(b) as being anticipated by Persson.

The applicant is familiar with this reference. Refer to fig. 2. An amplifier circuit is disclosed including a plurality of amplifier stages 170,190 that may be read on the like elements claimed. Then detectors 220,221 along with error detector 230 may be read as the power detector circuit claimed since the input to one detector (221) is at an interior node as claimed and the output signal will be based on the signals sampled by both detector elements, the inputs to the detector elements being read as the first and second inputs claimed. The last paragraph of the claim is met since the outputs of the detectors 220,221 are differenced by error detector 230.

Claims 39-46, 50-55 and 59 are rejected under 35 U.S.C. 102(e) as being anticipated by Chen et al.

The applicant is familiar with this reference. Refer to fig. 2. Treating independent claims 39,50 & 59 first, an amplifier circuit includes a plurality of amplifier stages 21,22,23 that may be read as the like elements claimed. Power detectors 41,42 sample the signal at interior nodes and together with bias control circuit 50, any of the outputs of which may be read as the first signal claimed, may be read as the power detector circuit claimed. Regarding the bias provision of claim 59 as well as claims 44 & 52, the outputs of circuit 50 also bias the amplifier stages and in regard to claims 46 & 54-55 will vary with the detected power. Regarding claims 40 & 42, amplifier 22 may be read as the intermediate amplifier stage and amplifier 23 as the final amplifier stage claimed with matching circuit 34 an output matching network as claimed. Regarding claims 43,45,51 & 53, either matching circuit 32 or 33 may be read on the matching network claimed. The nodes from which signals are detected, read as the interior node(s), appear to be within the matching networks.

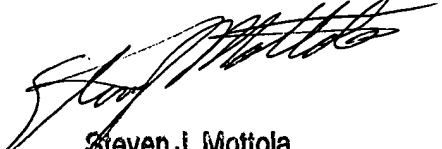
Claims 47-49, 56-58 & 61-62 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Claims 64-65 are allowed. Chen et al. do not describe summing, differencing or weighting the detector signals and Persson does not use the detected signal to bias the amplifiers.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Steven J. Mottola whose telephone number is 571-272-1766. The examiner can normally be reached on M-Th from 8 to 5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert J. Pascal, can be reached on 571-272-1769. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Steven J. Mottola  
Primary Examiner